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<b>Application Number</b>	<b>10/789,810</b>
<b>Filing Date</b>	<b>02/27/2004</b>
<b>First Named Inventor</b>	<b>Evgueni Goldberg</b>
<b>Art Unit</b>	<b>2129</b>
<b>Examiner Name</b>	<b>Omar F. Fernandez Rivas</b>
<b>Attorney Docket Number</b>	<b>CA7031042001</b>

[illegible][illegible]

5/2/2007

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STATEMENT BY APPLICANT**

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**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
/OFR/	2	BAPTISTA, L. et al.; "The Interplay of Randomization and Learning of Real-World Instances of Satisfiability"; Proceedings of the AAAI Workshop on Leveraging Probability and Uncertainty in Computation; July 2000.	
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Examiner Signature	/Omar Fernandez Rivas/		Date Considered 5/2/2007

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/OFR/	15	MARQUES-SILVA, J.; "The Impact of Branching Heuristics in Propositional Satisfiability Algorithms"; Proceedings of the 9th Portuguese Conference on Artificial Intelligence (EPIA); September 1999; LNAI; pp. 62 - 74; 1695; Portugal.	
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/OFR/	24	The SAT-Ex site; 2004; <a href="http://www.lri.fr/~simon/satex/satex.php3">http://www.lri.fr/~simon/satex/satex.php3</a> .	
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Examiner Name	Fernandez Rivas, Omar F.
Attorney Docket Number	CA7031042001

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/OFR/	1	GOLDBERG, E. "Testing Satisfiability of CNF Formulas by Computing a Stable Set of Points" Proceedings of the Conference Automated Deduction (CADE 2002) Copenhagen, DK, July 27-30, 2002, Vol. 2392, pp. 161-180	
	2	GOLDBERG, E. "Testing Satisfiability of CNF Formulas by Computing a Stable Set of Points" Annals of Mathematics and Artificial Intelligence, January 2005, Vol. 43, Issue 1-4, pp. 65-89	
	3	GOLDBERG, E. et al. "BerkMin: A Fast and Robust SAT-Solver" Proceedings of the design, Automation and Test in Europe Conference and Exhibition (DATE-2002) Paris, FR, March 4-8, 2002, pp. 142-149	
	4	GOLDBERG, E. "On Bridging Simulation and Formal Verification" Technical Report, University of California at Berkeley, February, 2007, CDNL-TR-2007-0212	
	5	Miroslav Velev's SAT Benchmarks, 3 pgs., located at: <a href="http://www.ece.cmu.edu/~mvelev/sat_benchmarks.html">http://www.ece.cmu.edu/~mvelev/sat_benchmarks.html</a>	
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	7	ABRAMOVICI, M. et al. "Digital Testing and Testable Design, Wiley-IEEE Press, September, 1990, Title page, Table of Contents and Chapters 2 through 10 (pp. 9-455).	
	8	SELMAN, B. et al. "A New Method for Solving Hard Satisfiability Problems" The 10th National Conference on Artificial Intelligence (AAAI-92), San Jose, CA, July 12-16, 1992, pp. 440-446	
	9	SELMAN, B. et al. "Noise Strategies for Improving Local Search" The 12th National Conference on Artificial Intelligence (AAAI-94), Seattle, WA, July 31-August 4, 1994, pp. 337-343	
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/OFR/	11	SETOVICH, E.M. et al. "SIS: A System for Sequential Circuit Synthesis" Technical Report, University of California at Berkeley, 1992. Memorandum No. UCB/ERL M92/41	
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